



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Vikram Saxena & Renu Mehra

Assignee: Synopsys, Inc.

Title: POWER DISSIPATION ESTIMATION SYSTEM AND METHOD AT THE REGISTER TRANSFER LEVEL, BEHAVIORAL LEVEL AND SYSTEM LEVEL

Serial No.: 09/771,322 File Date: January 26, 2001

Examiner: Chi Whan Chung Art Unit: 2115

Docket No.: SYN-0175

Date: June 16, 2004

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RECEIVED

JUN 28 2004

Technology Center 2100

AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Initial Comments

Claims 1-19 are pending in the present application. Claim 1 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite. Claims 1-2, 6, and 16 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bogliolo ("Parameterized RTL Power Model For Combinational Soft Macros"), Bhawmik (U.S. Patent 6,463,560), and Matsuo (JP 404131970). Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Bhawmik, Bogliolo, Matsuo, and Jochens ("A New Parameterizable Power Macro-Model For Datapath Components"). Claims 17 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bhawmik, Takahashi (U.S. Patent 6,523,157), Bogliolo, and Chen ("Architectural Level Hierarchical Power Estimation Of Control Units"). Claims 3-4, 7-15, and 19 are objected to as being

dependent upon a rejected base claim, but would be allowable if rewritten.

Claims 1, 17, and 18 are amended.